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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  <i>(Use as many sheets as necessary)</i>				Application Number: 10/755,042 Filing Date: January 9, 2004 First Named Inventor: Mou-Shiung Lin Art Unit: 2815 Examiner Name: Jerome Jackson Jr.	
Sheet	1	of	1	Attorney Docket No: 085027-0104	

**US PATENT DOCUMENTS**

Examiner Initials*	Cite No	Document Number	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		NONE			

**FOREIGN PATENT DOCUMENTS**

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		NONE				

**OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T
	1	"Electronic Materials Handbook, Volume 1 – Packaging: Other Design Considerations; Materials and Electronic Phenomena; Physical Characteristics of Microelectronic Materials" ASM International Handbook Committee, pgs. 104-111, ASM International (1989)	
	2	KUO, "Semiconductor Packaging Engineering", Zhan Yi-Zheng Publisher, Registration Taipei, April 2000, Table 9.1	

**EXAMINER**DM US 27832304-1,085027,0104  
0540TW, 0537TW NPL**DATE CONSIDERED**